

ABSTRACT

Digital signal processing section 103 has reconfigurable device 131, and reconfigurable device 131 configures decoding section 133 and coding section 134 by programming data. Decoding section 133 acquires synchronization from a signal output from reception section 121, and demodulates and decodes the signal. CPU 105 downloads programming data of another radio communication system to store in storage section 106 via general bus 104. Further, CPU 105 reads out the stored programming data, and reconfigures reconfigurable device 131. Storage section 106 stores the programming data.